

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10761927	
	Filing Date		2004-01-20	
	First Named Inventor	Vincenzo DiTommaso		
	Art Unit	2819		
	Examiner Name	Anh Q. Tran		
	Attorney Docket Number	1482-173		

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	1	Analysis and Design of Analog Integrated Circuits, Fourth Edition, Paul Gray, Paul J. Hurst, Stephen H. Lewis and Robert G. Meyer, 2001, pp. 317-317, Section 4.4.3 Temperature-Insensitive Biasing	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Joseph S. Makuch/	Date (YYYY-MM-DD)	2006-09-07
Name/Print	Joseph S. Makuch	Registration Number	39,286

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